

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 64-082615

(43)Date of publication of application : 28.03.1989

(51)Int.Cl.

H01L 21/205

C30B 29/42

H01L 21/76

(21)Application number : 62-242005

(71)Applicant : NEC CORP

(22)Date of filing : 25.09.1987

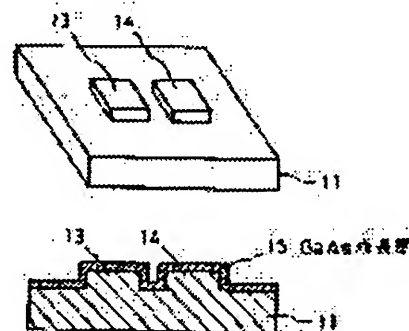
(72)Inventor : IWATA NAOTAKA

(54) MANUFACTURE OF SEMICONDUCTOR ELEMENT

(57)Abstract:

PURPOSE: To easily enable the electrical isolation between elements by exposing mesa structures having a face 110 and a face 111 on a group IV crystal substrate, and subsequently forming a III-V compound semiconductor layer by an atomic layer epitaxial growth method.

CONSTITUTION: Mesa structures 13, 14 having a face 110 and a face 111 are exposed on a group IV crystal substrate. And a III-V compound semiconductor layer 15 is formed an atomic layer epitaxial growth method. In the epitaxial growth on the face 110, point defects occur so that a deep level occurs in the semiconductor on the face 110 and the semiconductor is provided with a high resistance. Since the occurrence of point defects is low if an atomic layer epitaxial growth is performed on the face 111, little deep level occurs. Accordingly, the semiconductor on this face 111 can be controlled of the conductivity type and can be used as an active layer.



LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]